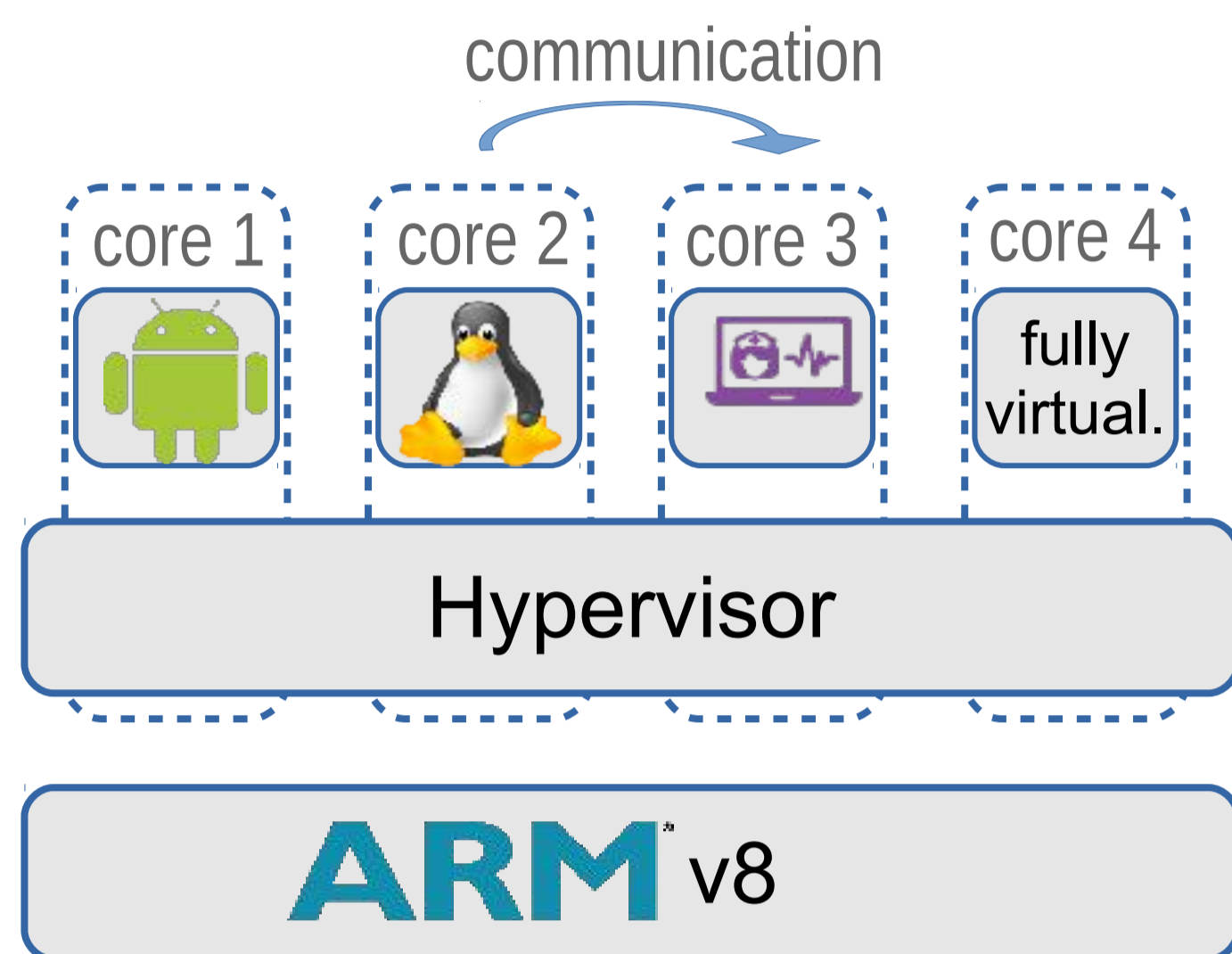
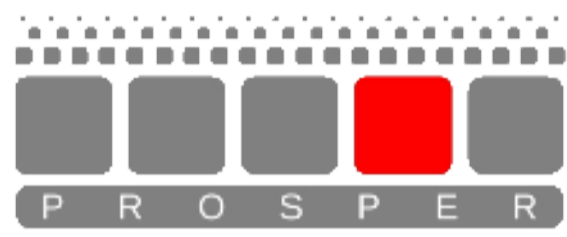


## Virtualization

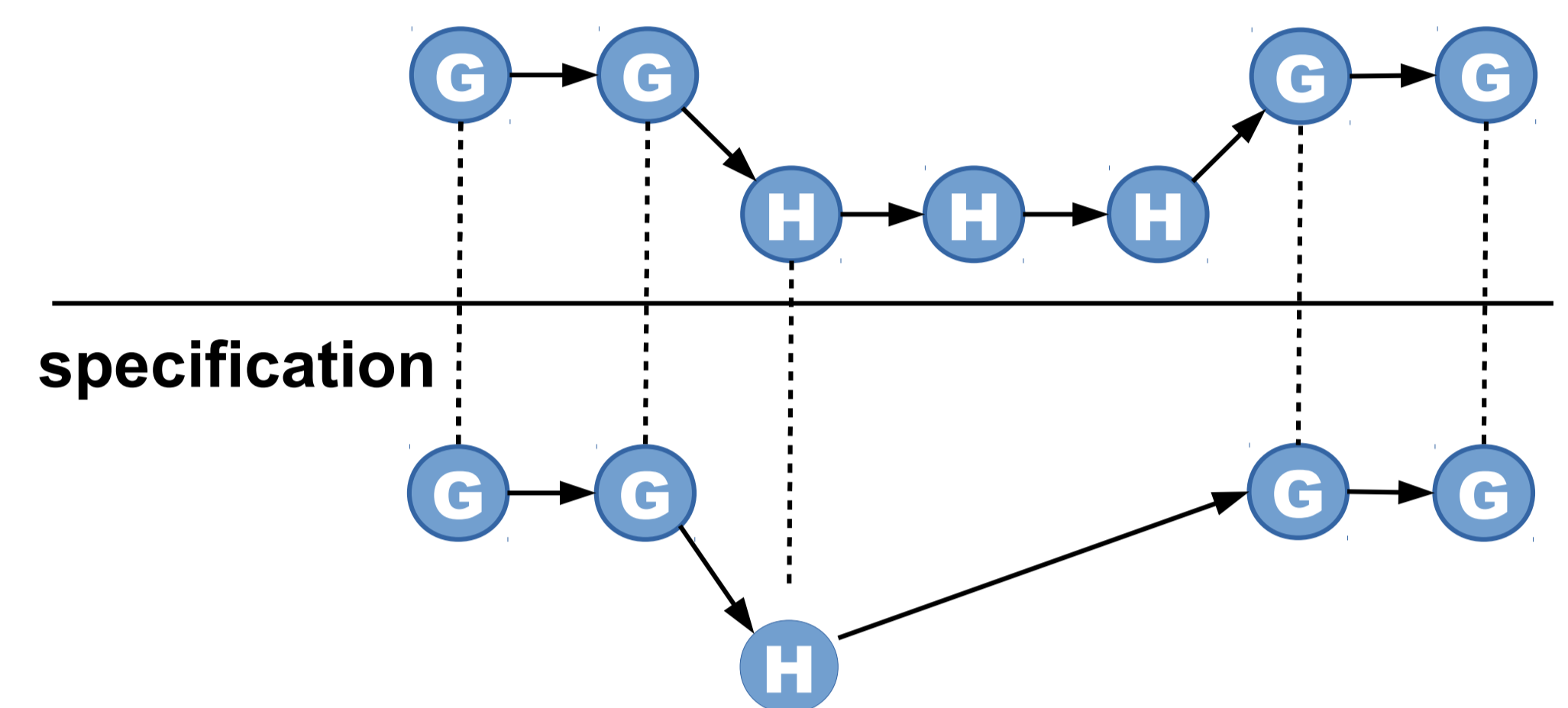


- main property: isolation
- open source
- successor of the SICS Thin Hypervisor from the SSF-funded PROSPER-project



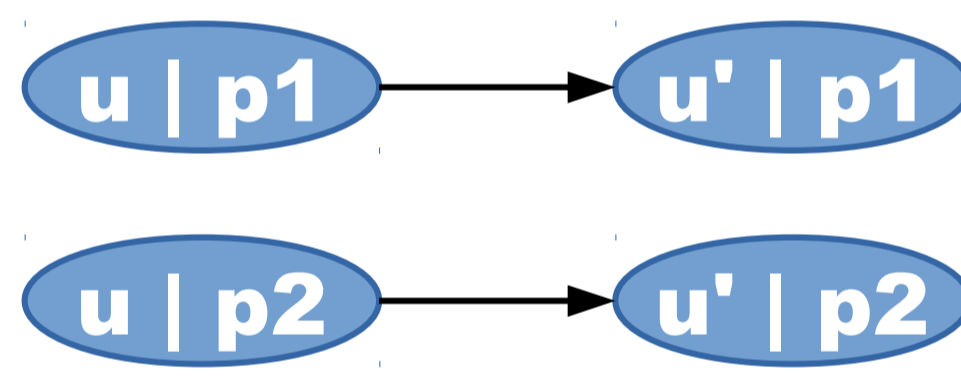
## Verification

real execution



### platform security

- integrity + confidentiality
- HOL4 theorem prover
- Cambridge models for ARM
- automated analysis
- multicore, memory, peripherals, interrupts, ...

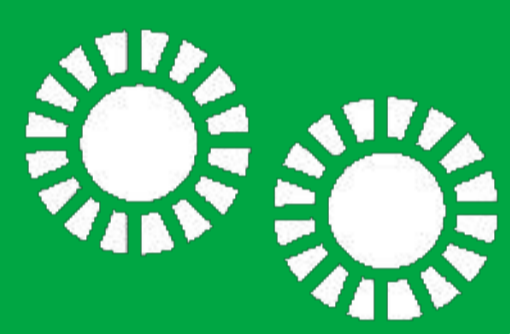


### hypervisor correctness

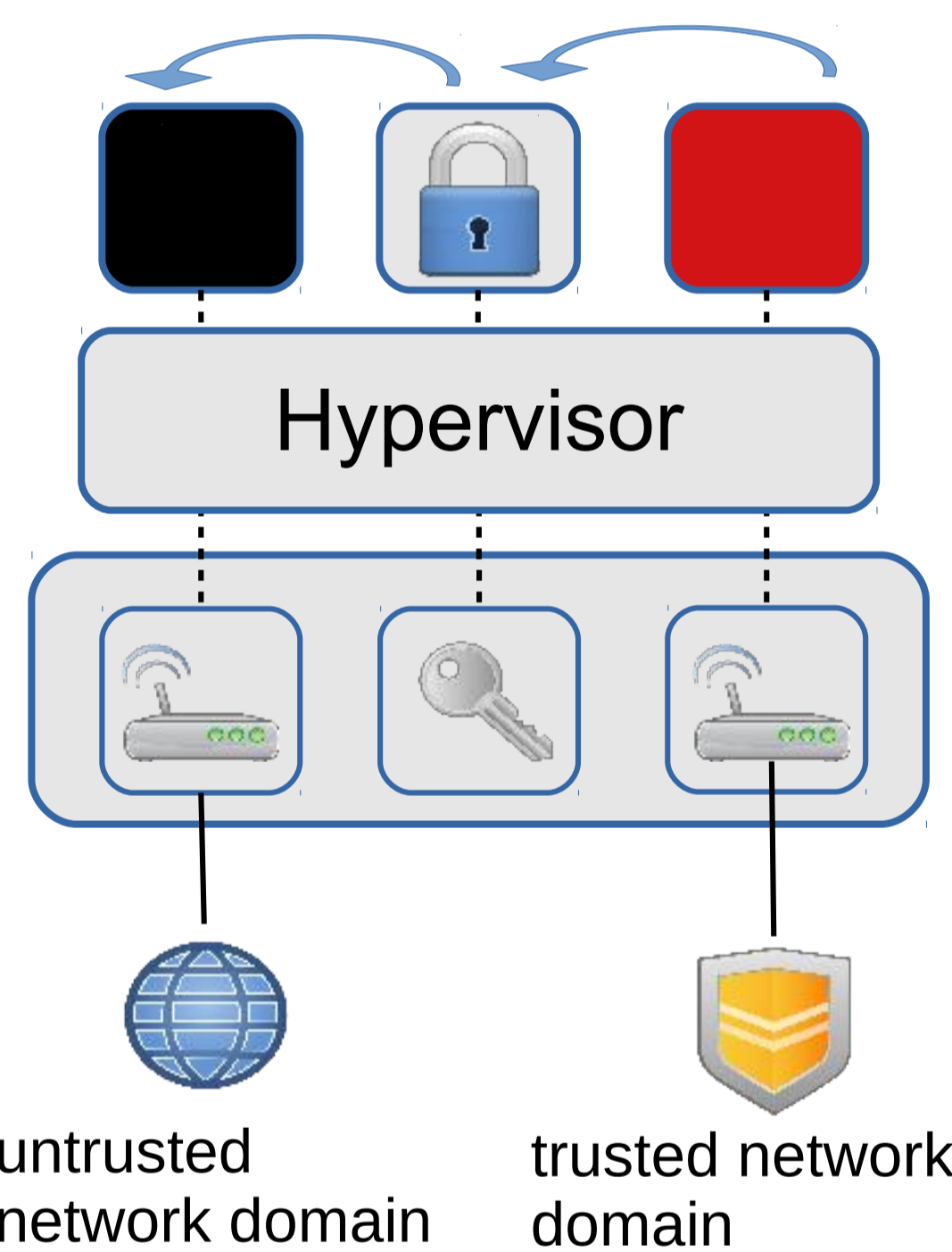
- functional correctness
- on binary level
- static analysis (BAP + SMT solvers)
- new tool to lift ARM
- binary to BAP code

boot

## Applications



- network security appliance (red/black separation)
- secure smart phone (business/private modes)
- SCADA systems, mobile communication networks, vehicular systems, IoT devices, ....



## Secure Boot

- basic initialization and hardware configuration
- assure integrity of hypervisor and guests
- assign resources
- chain of trust: ROM → boot loader → ... → hypervisor
- public RSA-keys tamper resistant



## CC Certification



- preparation for Common Criteria Evaluation
- EAL 6



## Contact

**Reference Group:**  
MSB, PTS,  
MUST, ABB

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